

METHOD OF SELF-ALIGNING AN OXIDE APERTURE WITH AN ANNULAR
INTRA-CAVITY CONTACT IN A LONG WAVELENGTH VCSEL

5 PRIORITY CLAIM:

This application claims the benefit of United States
Provisional Patent Application No. 60/394,035 filed July 6,
2002.

10 CONTINUATION STATUS:

This application is a continuation of United States
Patent Application No. 10/613,652 filed July 3, 2003.

FIELD OF THE INVENTION

15 This invention generally relates to optoelectronic
transmitters as used in optical communication systems, and
more particularly to long wavelength vertical cavity
surface emitting lasers which are used as the light source
is such devices.

20

BACKGROUND

A VCSEL is a semiconductor laser diode in which the
laser oscillation and the optical emission occur in a
direction normal to the p-n junction plane. VCSELs include
25 a semiconductor layer of optically active material, such as
gallium arsenide or indium phosphide sandwiched between
highly reflective mirror stacks. Conventionally, one of
the mirror stacks is partially reflective so as to pass a
portion of the coherent light which builds up in the
30 resonate cavity formed by the mirror stacks sandwiching the
active layer.

Lasing structures require optical confinement in the

resonating cavity and carrier confinement in the active region to achieve efficient conversion of pumping electron-hole pairs into stimulated photons through population inversion. In order to reach the threshold for lasing, the
5 total gain of a VCSEL must be equal to or greater than the total loss of the VCSEL. However, the compact nature of VCSELs typically limits the volume of gain media integrated into the device. Therefore, low-threshold, high-efficiency VCSEL operation typically requires a low loss index guiding
10 mechanism and highly reflective low loss mirrors.

SUMMARY

In one aspect of the present invention a method of forming a VCSEL having a plurality of layers includes
15 forming an ohmic contact adjacent an optical cavity, forming a mesa in at least a portion of the plurality of VCSEL layers in accordance with the ohmic contact to expose an oxide aperture layer and oxidizing the oxide aperture layer to form an oxide aperture that is aligned with the
20 ohmic aperture formed by the ohmic contact.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other features, aspects, and advantages of the present invention will become better understood with
25 regard to the following description, appended claims, and accompanying drawings, in which:

FIG. 1 is a cross-section of a VCSEL having a first mirror, an optical cavity having an active region and a second mirror and an oxide aperture current constriction in
30 accordance with an exemplary embodiment of the present invention;

FIG. 2 is a cross-section of the optical cavity of FIG. 1, wherein the cavity includes an active region with one or more active layers in accordance with an exemplary embodiment of the present invention;

5 FIG. 3 is a cross-section of a portion of the VCSEL of FIG. 1 graphically illustrating the overlap with the fundamental mode when the oxide aperture current constriction and the ohmic aperture are not aligned;

10 FIG. 4 is a simplified cross-section of the VCSEL of FIG. 1 wherein a two layer photoresist is deposited above the optical cavity in accordance with an exemplary embodiment of the present invention;

15 FIG. 5 is a cross-section of the VCSEL of FIG. 4 wherein the two layer photoresist has been patterned and a ohmic metalization layer has been deposited adjacent the optical cavity to form an intracavity contact in accordance with an exemplary embodiment of the present invention;

20 FIG. 6 is a cross-section of the VCSEL of FIG. 5 wherein a single layer photoresist is deposited above a portion of the intracavity contact and ohmic aperture in accordance with an exemplary embodiment of the present invention;

25 FIG. 7 is a cross-section of the VCSEL of FIG. 6 wherein the intracavity contact is used as an etch mask to define the outer walls of a mesa that exposes an oxide aperture layer in accordance with an exemplary embodiment of the present invention;

30 FIG. 8 is a cross-section of a VCSEL having an intracavity contact formed adjacent a current spreading layer above a delta doped upper cladding and a dielectric spacer layer adjacent the intracavity contact and current

spreading layer in accordance with an exemplary embodiment of the present invention;

FIG. 9 is a simplified cross section of the VCSEL of FIG. 8 having a two step photoresist formed adjacent the dielectric spacer layer in accordance with an exemplary
5 embodiment of the present invention;

FIG. 10 is a cross section of the VCSEL of FIG. 9 wherein the two step photoresist has been patterned and vias have been formed in the dielectric spacer layer to
10 allow for the formation of an intracavity contact adjacent the current spreading layer in accordance with an exemplary embodiment of the present invention;

FIG. 11 is a cross section of the VCSEL of FIG. 10 wherein an ohmic metalization layer has been deposited to
15 form the intracavity contact in accordance with an exemplary embodiment of the present invention;

FIG. 12 is a cross-section of the VCSEL of FIG. 11 wherein a single layer photoresist is deposited above a portion of the intracavity contact and ohmic aperture in
20 accordance with an exemplary embodiment of the present invention;

FIG. 13 is a cross-section of the VCSEL of FIG. 12 wherein the intracavity contact is used as an etch mask to define the outer walls of a mesa that exposes an oxide
25 aperture layer in accordance with an exemplary embodiment of the present invention;

FIG. 14 is a simplified cross-section of VCSEL having an ohmic metalization layer formed adjacent an optical cavity in accordance with an exemplary embodiment of the
30 present invention;

FIG. 15 is a simplified cross-section of the VCSEL of FIG. 14 wherein a photoresist layers has been deposited and

patterned adjacent the ohmic metalization layer in accordance with an exemplary embodiment of the present invention;

FIG. 16 is a cross-section of the VCSEL of FIG. 15 wherein the ohmic metalization layer has been processed to define an intracavity contact in accordance with an exemplary embodiment of the present invention;

FIG. 17 is a cross-section of the VCSEL of FIG. 16 wherein a second photoresist layer is deposited above a portion of the intracavity contact and first photoresist in accordance with an exemplary embodiment of the present invention;

FIG. 18 is a cross section of the VCSEL of FIG. 17 wherein the first photoresist layer is used as an etch mask to define the outer walls of a mesa that exposes an oxide aperture layer in accordance with an exemplary embodiment of the present invention;

FIG. 19 is a cross-section of a VCSEL having a hybrid upper mirror and an upper ohmic contact that is substantially aligned with an oxide aperture in accordance with an exemplary embodiment of the present invention; and

Figs. 20-26 show the step-by-step formation of a VCSEL having both the n-type and p-type ohmic contacts formed within the optical cavity, and wherein the oxide aperture is self-aligned with the p-type ohmic contact through the formation of the ohmic contact mesa as in the other exemplary embodiments of the invention.

DETAILED DESCRIPTION OF THE INVENTION

An exemplary embodiment of the present invention provides a high speed, low loss optoelectronic transmitter. One of the factors that limits the speed of a semiconductor

laser is the RC time constant of the device. The series resistance and optical loss of a conventional optoelectronic device may be limited in part by conduction through at least part of a high resistance DBR mirror.

5 In addition, current constrictions for transverse mode control may also limit the device series resistance. For example, ion implants are often utilized to constrict the current to a smaller diameter at the active area of the VCSEL relative to where it is injected at the contact.

10 In practice, ion implants typically have a Gaussian like vertical distribution, damaging the layers above and below the constriction thereby increasing the overall thickness of the constriction as well as the device resistance.

 Therefore, referring to FIG. 1, an exemplary
15 embodiment of the present invention may utilize an intracavity contact and oxide aperture to provide a high speed, low loss optoelectronic device. In the described exemplary embodiment a semiconductor light-emitting device
100 may comprise a lower mirror 112 adjacent a substrate
20 110, an upper mirror 114 above the lower mirror and an optical cavity 116, sandwiched between the upper and lower mirrors.

 The lower mirror of the light emitting device may comprise a plurality of epitaxially grown compound
25 semiconductor mirror periods. As is known in the art, the mirror periods may comprise, but are not limited to, one-quarter wavelength thick alternating layers of a high index of refraction material and a low index of refraction material. The lower mirror of such a device may be doped
30 n-type with a reflectivity of at least about 99%.

 Referring briefly to FIG. 2, the optical cavity may include an active region 208 surrounded by first and second

cladding regions 218 and 220. In an exemplary embodiment the active region may comprise one or more quantum wells 210(a-c) separated by barrier layers 212(a-b). The quantum-wells provide quantum confinement of electrons and holes therein to enhance recombination for the generation of the light, and may also include semiconductor layers comprising a plurality of quantum wires, quantum dots or other suitable gain material.

One of skill in the art will appreciate that the active layer(s) within the active region may be formed from a variety of material compositions. However, the proper selection of an active layer material preferably balances the gain requirements of the material with the mechanical stability of the device.

Referring back to FIG. 1, in the described exemplary embodiment an intracavity contact 118 may be formed above the optical cavity and a lower ohmic contact 146 may be formed below the optical cavity. In one embodiment the lower ohmic contact may be formed below the substrate 110 as illustrated in FIG. 1 or may be an intracavity contact to lower the resistance of the device. For example the lower ohmic contact in the various embodiments described below may have an intracavity arrangement such as those disclosed in commonly owned, copending U.S. Patent Application Serial No. 10/122,707, entitled "Long Wavelegnth Vertical Cavity Surface Emitting Laser", filed March 11, 2002, the content of which is incorporated herein by reference as if set forth in its entirety.

In the described exemplary embodiment the lower ohmic contact 146 may be an n-type ohmic contact comprising an n-type metalization such as AuGe/Ni/Au deposited to form either a full-surface metalization on the lower surface of

the substrate 110, or patterned to provide an annular aperture therethrough centered about the aperture in the optical cavity 116.

In operation, electrical current is conducted through the intra-cavity contact 118 (also referred to as an upper contact), then is laterally injected towards the aperture, and finally flows vertically through the aperture into the active region of the optical cavity 116 so that the upper mirror need not be conductive. Therefore, the upper mirror 114 may comprise a dielectric DBR formed from alternating one-quarter wavelength thick layers of a high index of refraction dielectric material and a low index of refraction dielectric material. For example, the upper mirror may comprise alternating layer of silicon nitride and silicon dioxide or other suitable dielectric materials. Advantageously, the use of the dielectric DBR may reduce the series resistance and optical loss that may otherwise be associated with a doped semiconductor upper mirror.

In the described exemplary embodiment an oxide aperture 122 is formed between the active region and the intracavity contact, and in certain embodiments functions to concentrate the current into a small area to thereby achieve the threshold current density with a smaller current. In an exemplary VCSEL structure, the oxide aperture may be formed by the steam oxidation of an Al-containing semiconductor layer. Water vapor exposure creates an oxide aperture some distance in from the sidewall toward the central vertical axis, depending on the duration of oxidation. The oxide aperture also introduces variation in the lateral refractive index and can provide a significant waveguiding effect in the transverse dimension. In alternate embodiments, the oxidized material may be

etched away or otherwise removed, forming an "air gap" aperture. Backfill material may then also be used in the formation of the aperture.

In practice, current confined devices may be susceptible to current crowding around the edges of the current aperture (e.g., oxide, implant, or other). Current crowding may result if the lateral conductivity is not small enough in relation to the vertical conductivity through the aperture. The effects of current crowding may be accentuated if the center of the oxide aperture is not adequately aligned with the aperture formed by the intracavity contact.

The oxidized outer portion of the oxide aperture generally has an annular shape with the oxidation extending symmetrically inward from the etched sidewalls of a mesa. The oxide aperture is therefore centered in the mesa. A lateral offset between the center of the oxide aperture and the center of the intracavity contact will therefore be created if the intracavity contact is not centered on the upper surface of the mesa.

Referring to FIG. 3, in practice a lateral offset between the oxide aperture 122 and the aperture formed by the intracavity contact 118 may create asymmetric resistance paths between the intracavity contact and the active region. The injected current will therefore tend to asymmetrically crowd along the paths of least resistance resulting in asymmetric pumping of the active region. In addition, the fundamental mode of the emitted optical beam tends to center itself about the highest injected current resulting in an offset between the center of the emitted beam and the center of the aperture formed by the intracavity contact. In operation, the intracavity contact

may therefore clip the emitted beam increasing the optical loss of the device.

Therefore, in the described exemplary embodiment the annular intracavity contact 118 may be formed on an upper
5 surface of the optical cavity prior to the formation of the mesa and patterned for use as an etch mask for the formation of the mesa. Advantageously, the use of the annular intracavity contact to define the outer wall of the mesa automatically aligns the center of the ohmic aperture
10 with the center of the oxide aperture.

For example, referring to the cross section of FIG. 4 in an exemplary process for producing VCSELs the intracavity contact may be formed from a lithographic masking and lift-off process. In the described exemplary
15 embodiment a multi-layer photoresist 410 and 420 may be deposited across the upper surface of the optical cavity 116. As will be appreciated by those skilled in the art, a single-layer photoresist may also be used. Referring to FIG. 5 the photoresist may be patterned and etched prior to
20 the deposition of the intracavity contact metalization 430.

In one embodiment the intracavity contact may be a p-type ohmic contact and may be formed by depositing a p-type metalization layer 430, above the optical cavity. The intra-cavity contact may be deposited by electron beam
25 evaporation or other techniques known in the art. In accordance with an exemplary embodiment, the multi-layer photoresist may be removed and a single step photoresist 440 may be formed by photolithography as illustrated in FIG. 6. In the described exemplary embodiment, the single
30 layer photoresist 440 overlaps onto a portion of the intracavity contact leaving the outer edge of the annular intracavity contact 118 exposed.

Referring to FIG. 7, a portion of the semiconductor layers at least down to the oxide aperture 122 may now be etched using the intracavity contact 118 as an etch mask to define the outer wall of the mesa. In an exemplary embodiment, a high density plasma etch system, such as, for example an inductively coupled plasma etch or the like may be used to form the mesa to reduce the etch time and minimize contact erosion. The described exemplary intracavity contact may be formed from a material composition that resists erosion during the plasma etch process.

For example, an exemplary intracavity contact 118 may comprise a layered structure, such as titanium and platinum or titanium, platinum and gold. In accordance with an exemplary embodiment, a reactive layer such as titanium may be utilized to couple with the semiconductor layers of the optical cavity and an inert layer, such as platinum may be deposited over the reactive layer to resist erosion during the mesa etch. In accordance with an exemplary embodiment the platinum may be in the range of about 1000-2000 angstroms thick and the titanium may be in the range of about 250-1000 angstroms thick. However, one of skill in the art will appreciate that the composition of the intracavity contact may vary as a function of the semiconductor material upon which it is deposited.

In the described exemplary embodiment the oxide aperture 122 may now be formed by placing the wafer into a container and heating the wafer to a temperature of about 350 to 500 °C under a controlled environment having a high humidity. Such a moist environment may be generated, for example, by flowing a gas, such as nitrogen, through water

heated to about 80-95 °C to entrain water vapor, and then directing the moisture-laden gas into the container.

The described exemplary intracavity contact may therefore also be resistant to oxidation which may occur
5 during the formation of the oxide aperture. Otherwise a protective coating may be deposited over the intracavity contact to prevent oxidation of the contact during the formation of the oxide aperture. In the described exemplary embodiment of Figure 1, the upper mirror 114 may be formed
10 adjacent the optical cavity 116 and the intra-cavity contact 118.

Referring to FIG. 8, in another embodiment of the present invention an optical cavity may 510 include a delta doped upper cladding 520 for conducting holes into the
15 active region to cause lasing as disclosed in commonly owned, copending U.S. Patent Application Serial No. 10/105,473 entitled " HYBRID VCSEL WITH BURIED INTERFACE", filed March 25, 2002, the content of which is incorporated herein by reference as if set forth in its entirety.

20 The delta doped upper cladding 520 may be bulk doped with a suitable p-type dopant, such as, for example, C or Be at a relatively low density. In one embodiment, the delta doped upper cladding layer 520 may include one or more p-type doping spikes 532(a) and 532(b) located at or
25 near nulls in the optical standing wave pattern.

Advantageously, the placement of the p-type doping spikes, at or near nulls in the standing wave intensity pattern where the optical losses are near zero, provides lateral conduction from the intra-cavity contact with
30 relatively low resistance. One of skill in the art will appreciate that the p-type doping spikes in the delta doped

upper cladding may not be necessary in some designs where device resistance is not a limiting constraint, although it is nevertheless likely that current crowding will occur symmetrically at the edges of the aperture if the doping
5 spikes are absent.

In the described exemplary embodiment, the bulk doped p-type material of the delta doped upper cladding 130 has a relatively low dopant concentration. The delta doped upper cladding may not therefore have sufficient conductivity to
10 provide a low resistance intra-cavity contact 118.

Therefore, in one embodiment, a contact layer 550 may be deposited on the uppermost surface of the delta doped upper cladding 520. In the described exemplary embodiment the contact layer is highly conductive, with a p-type doping
15 density greater than about $1 \times 10^{19} \text{cm}^{-3}$. The intra-cavity contact 118 may then be deposited on the more heavily doped contact layer 550. In accordance with an exemplary embodiment, the contact layer 550, in combination with p-type doping spikes 532(a) and 532(b), may also act as a
20 current spreading layer to provide a more uniform current distribution across the ohmic aperture, improving current injection into the active region of the optical cavity.

In a conventional structure a low index of refraction dielectric layer would be formed proximate the p-type
25 contact layer and the mirror terminates in a high index layer. In operation, the large step down in the index of refraction at the interface between the uppermost high index of refraction layer and free space creates a maximum in the optical standing wave at this interface. In
30 addition, the alternating layers are typically stacked such that the maxima in the optical standing wave intensity are

located at the high-to-low index steps as seen from the optical cavity.

Therefore, conventional structures also result in the formation of a maximum of the optical standing wave intensity pattern at the interface between the contact layer 550 and the dielectric upper mirror 114. Therefore, losses from the highly doped contact layer and light scattering defects, such as for example non-smooth surfaces, are magnified in a conventional design.

In an exemplary embodiment of the present invention, a dielectric spacer layer 560 may therefore be formed adjacent to the contact layer 550. In the described exemplary embodiment the dielectric upper mirror 114 may be formed above the dielectric spacer layer 560.

The integration of the dielectric spacer layer 560 adjacent the contact layer 550 functions to form a hybrid cavity. In accordance with an exemplary embodiment the undesirable interface between the contact layer 550 and the first dielectric layer is relocated to be at or near a null in the standing wave intensity pattern. Therefore, in operation the optical wave has reduced interaction with the imperfections on the surface of the semiconductor material and experiences lower loss from the highly doped contact layer 550 formed thereon.

Advantageously, the dielectric spacer layer 560 may also be utilized to form a beam guide as disclosed in commonly owned co-pending U.S. Patent Application Serial NO. 10/186,779, filed July 1, 2002, entitled "ANTIGUIDE SINGLE MODE VERTICAL CAVITY LASER" the content of which is incorporated herein by reference as if set forth in full. Index modulation techniques generally tailor the real part of the refractive index laterally so as to form a waveguide

that concentrates the beam, leading to higher efficiency,
lower threshold and a greater tendency for multimode
operation. The waveguide in the index modulation
techniques typically involves a region of higher index of
5 refraction and a region of lower index of refraction. The
relative index step determines the radius of the waveguide
for single mode operation as follows:

$$r = \frac{2.405\lambda}{2\pi\sqrt{n_1^2 - n_2^2}}, \quad (\text{Eq.1})$$

where λ is the lasing wavelength and n_1 and n_2 are the
10 effective (or weighted average) indexes of refraction along
the entire length of the cavity inside and outside the
radius of the optical aperture, respectively. As seen from
the equation the greater the index step between the
effective indices of refraction, the smaller the single
15 mode cutoff radius.

Conventional oxide apertures typically create a
relative high index step and therefore require relatively
small electrical and optical apertures to achieve single
mode operation. Referring to FIG. 8, in an exemplary
20 embodiment of the present invention the thickness of the
oxide aperture may be reduced and its position adjusted to
be at or near a null in the standing wave, so as to reduce
the index step seen by the optical mode. The described
exemplary dielectric spacer layer 560 may then be utilized
25 to form a second index guide with a different radius than
the oxide aperture. Advantageously, the reduced index step
due to the oxide aperture allows for the utilization of a
larger diameter oxide aperture while maintaining single-
mode operation. The larger aperture lowers the overall
30 electrical resistance of the device. The second index

step, at a larger radius formed in the spacer layer 560, can then concentrate the light and still maintain single-mode operation.

The feature formed in the spacer layer 560 may also be simultaneously laterally aligned with the intracavity contact and the oxide aperture. For example, referring to FIG. 9, the contact layer 550 and dielectric spacer layer 560 may first be formed above the compound semiconductor optical cavity 510. In the described exemplary embodiment a multi-layer photoresist 570 and 580 may be deposited adjacent the dielectric spacer layer 560. Referring to FIG. 10, the photoresist may be patterned and etched prior to the deposition of the intracavity contact metalization 590 (FIG. 11). In the described exemplary embodiment, the first layer 570 of the multi-layer photoresist and the dielectric spacer layer 560 may be undercut i.e. laterally etched to a greater extent than the second layer 580 of the multi-layer photoresist.

The intracavity contact metalization 590 may now be deposited as illustrated in FIG. 11. In the described exemplary embodiment the via in the second layer 580 of the multi-layer photoresist defines the intracavity contact 518. The intracavity contact 518 may again comprise a layered structure, such as, titanium and platinum wherein the titanium provides good electrical coupling with the semiconductor layers and the platinum resists erosion during the mesa etch. In the described exemplary embodiment, the intracavity contact 518 and the dielectric spacer layer 560(a) within the intracavity contact aperture are defined by the same photoresist mask 580(a) and are therefore automatically aligned.

In accordance with an exemplary embodiment, the multi-layer photoresist may be removed and a single step photoresist 600 may be formed by a lithographic masking and lift off process as illustrated in FIG. 12. In the
5 described exemplary embodiment, the single layer photoresist 600 overlaps onto a portion of the intracavity contact 518 leaving the outer edge of the annular intracavity contact 518 exposed.

Referring to FIG. 13, a portion of the semiconductor
10 layers at least down to the oxide aperture 122 may now be etched using the intracavity contact as an etch mask to define the outer wall of the mesa. In an exemplary embodiment a high density plasma etch system, such as, for example an inductively coupled plasma etch or the like may
15 be used to form the mesa to reduce the etch time and minimize contact erosion.

In an exemplary embodiment the oxide aperture 122 may again be formed by placing the wafer into a container and heating the wafer to a temperature of about 350 to 500 °C
20 under a controlled environment having a high humidity. The resulting aperture, the ohmic contact and the feature in layer 560 are automatically aligned.[add'l figure?] In the described exemplary embodiment the upper dielectric mirror 114 may be formed adjacent the dielectric spacer layer 560,
25 the contact layer 550, and the intra-cavity contact 518.

One of skill in the art will appreciate that the present invention is not limited to particular material systems or emission wavelengths. Rather, the compound semiconductor layers of the described exemplary light-
30 emitting device may be formed from a variety of group III-V or II-VI compound semiconductors, such as, for example,

GaAs/AlGaAs, InGaAs/AlGaAs or InP/InGaAsP or other direct bandgap semiconductor materials.

In addition, the present invention is not limited to photolithographic and lift off processes for the deposition
5 of the described exemplary intracavity contact. Rather, the intracavity contact may be formed in accordance with a subtractive etch process. For example, referring to FIG. 14, an intracavity contact metalization layer 700 may be formed adjacent the optical cavity 510. A photoresist 710
10 may then be deposited above the intracavity contact metalization layer 700. The photoresist 710 may then be patterned as illustrated in FIG. 15 and the intracavity contact metalization layer may be etched to define the intracavity contact 718 as illustrated in FIG. 16.

15 Referring to FIG. 17, in an exemplary embodiment a second photoresist 740 may be deposited above the aperture formed by the intracavity contact and the first photoresist 710. In the described exemplary embodiment the second photoresist 740 overlaps onto a portion of the first
20 photoresist 710 leaving the outer edge of the annular first photoresist 710 exposed.

Referring to FIG. 18, a portion of the semiconductor layers at least down to the oxide aperture 122 may now be etched using the first photoresist as an etch mask to
25 define the outer wall of the mesa. In an exemplary embodiment a high density plasma etch system, such as, for example an inductively coupled plasma etch or the like may be used to form the mesa to reduce the etch time and minimize erosion of the first photoresist 710.

30 In the described exemplary embodiment the oxide aperture 122 may again be formed by placing the wafer into a container and heating the wafer to a temperature of about

350 to 500 °C under a controlled environment having a high humidity. In this embodiment the walls of the mesa and the intracavity contact are again defined by the same photoresist, thereby ensuring alignment between the oxide aperture and the intracavity contact. In the described exemplary embodiment an upper mirror 114 again may be formed adjacent the optical cavity 510 and the intra-cavity contact 718.

To those skilled in the various arts, the invention itself herein will suggest solutions to other tasks and adaptations for other applications. For example, the present invention is not limited to VCSELs having intracavity contacts and dielectric upper mirrors. For example, referring to FIG. 19, the present invention may be utilized to align an upper ohmic contact 818 formed on a semiconductor portion 830 of a hybrid upper mirror with an oxide aperture 822.

In this embodiment, the number of mirror periods in the semiconductor portion of the hybrid mirror may be restricted to minimize optical loss, resistance etc. associated with conduction through the mirror. A dielectric mirror portion 840 may then be used to provide the reflectance required of the upper hybrid mirror. In this embodiment, the upper ohmic contact 818 may again be self aligned to the oxide aperture 822 to reduce resistance and optical loss.

Turning now to Figs. 20-26, another exemplary embodiment is illustrated wherein both the n-ohmic contact and the p-ohmic contact are formed within the optical cavity. The completed VCSEL structure is generally indicated at 900 in Fig. 26. In the described exemplary

embodiment, the VCSEL 900 may comprise a substrate 910, lower mirror 912 adjacent a substrate 910, an upper mirror 914 above the lower mirror and an active optical layer 916, sandwiched between the upper and lower mirrors.

5 In the present embodiment, the lower mirror 912 comprises an undoped mirror structure comprising alternating layers as known in the art. Built up on top of the lower mirror is a highly doped n-type contact layer 918. This will form the contact layer for the n-type ohmic
10 contact pad 920. Accordingly, it can be seen that the n-type contact layer 918 is located within the optical cavity between the lower mirror 912 and the active layer 916. Located above and below the active layer 916 are two spacer (cladding) layers 922 and 924. The active layer 916
15 includes quantum wells are previously described hereinabove.

 An oxide aperture layer 926 is formed above the spacer layer 924, and another spacer layer 928 is formed on top of the oxide aperture layer 926.

20 The p-type ohmic contact layer 930 (p-stack layer) is formed above spacer layer 928, and forms the contact surface for the p-type ohmic contact pad 932. As described hereinabove, the p-stack layer 930 contains one or more doping spikes 934 located at the null points in the
25 standing wave pattern. The doping spikes 934 force electrons to move horizontally in the layer so that they can more easily reach the aperture in a uniform distribution. It is noted that the uppermost layer of the p-stack is preferably a spike layer for better conduction.
30 However, placement of the spike layer at the top of the p-stack 932 forces the device to be out of phase, and thus forces the use of a re-phase layer (puck) illustrated at

936. The re-phase puck 936 put the standing wave back in phase before passing through the upper mirror structure 916. It is once again noted that the relative thicknesses of the respective layers are not to scale and are intended
5 only to illustrate the described features.

Step-by-step construction of the VCSEL 900 is illustrated in Figs. 20-25 showing build-up and etching of the layers using the self-aligning oxide aperture method described above.

10 Turning to Fig. 20, a the device is monolithically built up layer by layer up through the re-phase layer 936. A two-part photoresist 938 and 940 is then applied on top of the re-phase layer 936.

Referring now to Fig. 21, the photoresist 938 and 940
15 may be patterned and etched prior to the deposition of the intracavity contact metalization 932 (p-type) (see Fig. 22). It is noted that the pattern and etch of the photoresist form the basis for all of the future self-aligned device features. The pattern and etch of the
20 photoresist first forms the central re-phase puck 936 which is located on the optical axis of the device. The intracavity contact pad 932 deposited within the etch channel (Fig. 22) is automatically concentrically self-aligned with the re-phase puck 936 when deposited.

25 Turning Fig. 23, the photoresist 938 and 940 is removed, leaving the rephase puck 936 and the p-type contact pad 932. A single step photoresist 942 is formed over the rephase puck 936 and contact pad 932 as illustrated in Fig. 23. In the illustrated embodiment, the
30 photoresist 942 overlaps onto a portion of the intracavity contact pad 932 leaving the outer edge of the contact 932 exposed.

Referring now to Fig. 24, a portion of the semiconductor layers at least down to the n-doped contact layer 918 may now be etched using the intracavity contact 932 as an etch mask to define the outer wall of the mesa.

5 Still referring to Fig. 24, the self-aligned oxide aperture 926a may now be formed by placing the wafer into a container and heating the wafer to a temperature of about 350 to 500 °C under a controlled environment having a high humidity. Once again, the oxide aperture 926a self-aligns

10 with the ohmic contact 932 and the rephase puck 936 because of the uniform inward radial oxidation of the oxidation layer 926 as defined by the outside surface of the mesa.

In a final series of steps, the photoresist 942 is removed, the n-type ohmic contact pad 920 is deposited onto

15 the n-type contact layer (Fig. 25), and the upper mirror structure 914 is deposited onto the top of the device (Fig. 26).

It is therefore desired that the present embodiments be considered in all respects as illustrative and not

20 restrictive, reference being made to the appended claims rather than the foregoing description to indicate the scope of the invention.